R15 Code No: 127CZ JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech IV Year I Semester Examinations, November/December - 2018 EMBEDDED SYSTEM DESIGN (Common to ECE, ETM) Max. Marks: 75 Time: 3 Hours Note: This question paper contains two parts A and B. Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions. (25 Marks) [2] What is "Time-to-prototype"? 1.a) What is the difference between a system and an embedded system? [3] b) What is memory shadowing? What is its advantage? [2] c) What are the uses of ASIC in designing an embedded system? [3] d) [2] What is 'Assembly Language" programming? e) Explain the need of a watchdog timer. f) Why is thread creation faster than process creation? [2] g) Which operating system is suitable for embedded system design? Explain. [3] h) [2] What is priority inversion? i) [3] Give the features of RTOS. j) (50 Marks) What is an embedded system? Explain the different applications of embedded systems. 2. [10] Explain quality attribute in the embedded system development context? What are the different quality attributes to be considered in an embedded system design. [10] What is the difference between big-endian and little-endian processors? Give an example 4. [10] of each? OR

Explain the different on-board communication interfaces in brief.

6.a)

b)

development.

Explain the advantages of 'Assembly language' based embedded firmware development.

Explain the advantages of 'High Level language' based embedded firmware

OR

[5+5]

